

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Patent No. 09/980,027
Issued: 02/27/02
Application No.: 09/980,027
Filed: 02/27/02
Inventor(s): MORCHE,
DOMINIQUE

Title: BAND-PASS FILTER WITH CARRIER FREQUENCY REDUCTION

Examiner:
Group/Art Unit:
Atty. Dkt. No: 5957-08800

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

B. Noël Kivlin

Printed Name

Signature

Date

**TRANSMITTAL OF POWER OF ATTORNEY AND
NOTICE OF CHANGE OF ADDRESS**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Please find attached a Power of Attorney with regard to the above-identified patent application. Applicant respectfully requests the Commissioner to change the correspondence address for the above identified patent application. The old correspondence address was:

Eric S. Hyman
Blakley Sokoloff Taylor & Zafman
12400 Wilshire Boulevard 7th Floor
Los Angeles, CA 90025

The new correspondence address is:

B. Noël Kivlin
Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C.
P.O. Box 398
Austin, Texas 78767-0398
(512) 853-8840

If there are any questions regarding this matter, please contact me at the telephone number provided below.

Respectfully submitted,



B. Noël Kivlin
Reg. No. 33,929
ATTORNEY FOR APPLICANTS

Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C.

P.O. Box 398

Austin, TX 78767-0398

(512) 853-8800

Date: 6-9-05



5957-05500
PATENT

POWER OF ATTORNEY; NOTICE OF CHANGE OF ADDRESS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

POWER OF ATTORNEY

FAHRENHEIT THERMOSCOPE LLC hereby revokes any previous Powers of Attorney and appoints the following attorneys and/or agents in connection with any and all patent applications and patents, owned by FAHRENHEIT THERMOSCOPE LLC, that are filed with the United States Patent and Trademark Office: (1) the practitioners at Customer Number 35690; and (2)

Mark K. Brightwell	Reg. No. 47,446
Kay A. Colapret	Reg. No. 52,759
Steve J. Curran	Reg. No. 50,664
Mark R. DeLuca	Reg. No. 44,649
Heather L. Flanagan	Reg. No. 54,101
Russell Henrichs	Reg. No. 50,354
Erik A. Heter	Reg. No. 50,652
Jeffrey C. Hood	Reg. No. 35,198
Rajiv Jauhari	Reg. No. 55,850
B. Noël Kivlin	Reg. No. 33,929
Robert C. Kowert	Reg. No. 39,255
Mario J. Lewin	Reg. No. 54,268
Lawrence J. Merkel	Reg. No. 41,191
Eric B. Meyertons	Reg. No. 34,876
Neal E. Persky	Reg. No. 53,452
Liza Philip	Reg. No. 51,352
David W. Quimby	Reg. No. 39,338
Rory D. Rankin	Reg. No. 47,884
Gareth Sampson	Reg. No. 52,191
Chris Thompson	Reg. No. 43,188
Mark S. Williams	Reg. No. 50,658

each an attorney or agent of the firm of MEYERTONS, HOOD, KIVLIN, KOWERT & GOETZEL, P.C., as its attorney or agent for so long as they remain with such firm, with full power of substitution and revocation, to prosecute the application, to make alterations and amendments therein, to transact all business in the Patent and Trademark Office in connection therewith, and to receive any Letters Patent, and for one year after issuance of such Letters Patent to file any request for a certificate of correction that may be deemed appropriate.

Please direct all communications as follows:

Customer No. 35690
B. Noël Kivlin, Esq.
MEYERTONS, HOOD, KIVLIN, KOWERT & GOETZEL, P.C.
P.O. BOX 398
AUSTIN, TEXAS 78767-0398
(512) 853-8840 (voice)
(512) 853-8801 (facsimile)

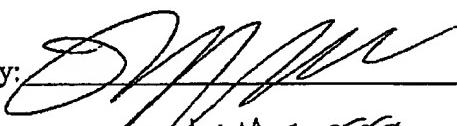
CHANGE OF ADDRESS

Applicant respectfully requests the Commissioner to change the correspondence address for the any and all patent applications and patents filed by FAHRENHEIT THERMOSCOPE LLC.

Applicant's new correspondence address is:

Customer No. 35690
B. Noël Kivlin
Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C.
P.O. Box 398
Austin, Texas 78767-0398
(512) 853-8840

If there are any questions regarding this matter, please contact B. Noël Kivlin at the telephone number provided.

By: 
Printed Name: ANNA M. UEFFING
Title: ANALYZED PERSON
Date: 07 JUN 2005

BEST AVAILABLE COPY

ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, France Telecom S.A. having offices at France Telecom R&D, 38-40 rue du Général Leclerc, 92794 Issy les Moulineaux Cedex 9, France (which France Telecom S.A. is the transformation of *France Telecom, Etablissement autonome de droit Public*, 6, Place d'Alleray, F-75015 Paris, France, by virtue of French law 96-660 of July 26, 1996), ("Assignor"), does hereby sell, assign, transfer and convey unto Fahrenheit Thermoscope LLC a Delaware limited liability company, having an office at 2215-B Renaissance Drive, Suite 5, Las Vegas, NV 89119 ("Assignee") or its designees, all of Assignor's right, title and interest in and to the patent applications and patents listed below, any patents, registrations, or certificates of invention issuing on any patent applications listed below, the inventions disclosed in any of the foregoing, any and all counterpart United States, international and foreign patents, applications and certificates of invention based upon or covering any portion of the foregoing, and all reissues, re-examinations, divisionals, renewals, extensions, provisionals, continuations and continuations-in-part of any of the foregoing (collectively "Patent Rights"):

Assignor further agrees to and hereby does sell, assign, transfer and convey unto Assignee all rights that it possesses: (i) in and to causes of action and enforcement rights for the Patent Rights including all rights to pursue damages, injunctive relief and other remedies for past and future infringement of the Patent Rights, and (ii) to apply in any or all countries of the world for patents, certificates of invention or other governmental grants for the Patent Rights, including without limitation under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement or understanding. Assignor also hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents or certificates of invention which may be granted upon any of the Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

Assignor will, at the reasonable request and sole expense of Assignee and without demanding any further consideration therefor, do all things necessary, proper, or advisable, including without limitation the execution, acknowledgment and recordation of specific assignments, oaths, declarations and other documents on a country-by-country basis, to reasonably assist Assignee in obtaining, perfecting, sustaining, and/or enforcing the Patent Rights. Such assistance shall include, to the extent Assignor is reasonably capable of doing so, providing, and obtaining from the respective inventors, prompt production of pertinent facts and documents, giving of testimony, execution of petitions, oaths, powers of attorney, specifications, declarations or other papers and other assistance reasonably necessary for filing patent applications, complying with any duty of disclosure, and participating in prosecution, reexamination, reissue, interference or other priority proceedings, opposition proceedings, cancellation proceedings, public use proceedings, infringement or other court actions and the like with respect to the Patent Rights.

The terms and conditions of this Assignment shall inure to the benefit of Assignee, its successors, assigns and other legal representatives, and shall be binding upon Assignor, its successor, assigns and other legal representatives.

KJ

BEST AVAILABLE COPY

<u>Filing Number</u>	<u>Country Code</u>	<u>Filing Date</u>	<u>Page 1 of 7</u> <u>Title / Inventors</u> <u>(US Patent)</u>
16991600	US	14/05/1991	Methods for manufacturing an active matrix display screen with storage capacitors / Morimoto, Connelly
2641100	US	22/06/1991	Active matrix display screen with storage capacitors formed of conductive blocks, semiconductive material, nonconductive material and capacitive lines / Morimoto, Connelly
7922262	US	5/4/15/11/1991	Method for etching integrated circuit layers of a fixed depth and corresponding integrated circuit / Itoh
091403055/6	DE	14/11/1991	
901143061	FR	16/11/1990	
091403055/6	FR/EP	14/11/1991	
91108055/6	GB	14/11/1991	
7642101	US	23/09/1991	Protective circuit for a control circuit of a liquid crystal display screen / Senn, Loyal, Mirel
7641961	US	23/09/1991	Simple and reliable connection of liquid crystal display screen / Senn, Loyal, Mirel
3856248	US	18/02/1992	Collector-gated polysilicon complementary MOS technology / Nomura, Suzuki
92220047/0	EP	12/02/1992	
91101934	FR	12/02/1992	
09124078	US	06/02/1992	Process of planar local oxidation of polysilicon and corresponding integrated circuit / Shapira, Kadar, Wulfblum
09101381	EP/FR	07/02/1992	
3866542	US	10/04/1992	Process for the production of thin film transistors/a diode
09107618	US	18/07/1992	Method of manufacturing a vertical field effect transistor / Chintalapudi, Nourinia
092401909/4	DE	08/07/1992	
092401909/4	GB	03/07/1992	
9108677	FR	10/07/1991	
19111013	US	09/07/1992	Active matrix high definition liquid crystal display structure / Morimoto, Chouani, Vionze
02201947/0	GB	09/07/1992	
12041236	JP	09/07/1992	
02240193/10	NL	09/07/1992	

F5

BEST AVAILABLE COPY

<u>Filing Number</u>	<u>Country Code</u>	<u>Filing Date</u>	<u>Title / Inventors (US Patent)</u>
2311024	US	20/04/1994	Method of clock signal economy and synchronization of the reception of information signals transmitted via an ATM network and devices for implementation of the method / Kurnar
924100854	DE	19/04/1994	
93104775	FR	22/04/1993	
92410085B	GB	19/04/1994	
228479	US	15/04/1994	Signal processing device using several different filterings especially for audio frequency coding of voice signals / Bilesford, Senn
924100780,6	DE	11/04/1994	
931041532	FR	16/04/1993	
924100780,6	GB	11/04/1994	
2795346	US	25/07/1994	Optical amplifier having a doped fiber degassing tube and process for producing this amplifier / Ronat, El-Guiberti, Lemoine
924101706,0	GB	25/07/1994	
93109105	FR	26/07/1993	
924101706,0	DE	25/07/1994	
924101706,0	IT	25/07/1994	
15991668	US	02/09/1994	Process for the transmission of optical signals modulation of one or more wavelengths by another adjustable wavelength disposed upstream / Alzola, Lemoine
941101984,3	GB	10/09/1994	
924101984,3	DE	10/09/1994	
93110737	FR	10/09/1993	
1861958	US	10/02/1995	Degassing device, particularly for optical fibres / Grosjean, Gauvrit
92410167,1	FR	11/02/1994	
95400232,5	DE	03/02/1995	
95400232,5	GB	03/02/1995	
95400232,5	IT	03/02/1995	
1111777472	US	24/01/1995	Optical amplifier with a doped fluoride glass or optical fibres and process for producing this amplifier / Semikoff, Ronat, El-Guiberti
1111925391	US	20/05/1995	Electron memory addressing device, especially for memory chips, in particular for mobile phones / Omilley, Duff
95401444,5	DE	20/05/1995	
94107579	FR	21/06/1994	
95401444,5	GB	20/05/1995	

5

BEST AVAILABLE COPY

<u>Filing Number</u>	<u>Country Code</u>	<u>Filing Date</u>	<u>Page 3 of 7</u> <u>Title / Inventors</u> <u>(US Patent)</u>
5641120	US	30/03/1995	Electrically conductive polymer compositions, production process and coated substrates // Carisse, Delangle et al.
959152307	GB	30/03/1995	Coprint
059152807	DE	30/03/1995	
94103812	FR	17/03/1994	
5271137	US	12/09/1995	Sequential process synchronous memory device and corresponding process for storage and reading // Major, Well et al.
954022135	DE	04/10/1995	
94112170	FR	12/10/1994	
954022135	GB	04/10/1995	
5393191	US	10/12/1995	Electrical component capable of being integrated on a substrate and method of manufacturing the same // Dittell, Robert et al.
94115504	TR	22/12/1994	
954028093	DE	13/12/1995	
954028093	GB	13/12/1995	
6391081	US	21/04/1995	Device for programmable delay of an analog signal and corresponding acoustic antenna // Jonsson, Balstad et al.
964008510	FR/EP	22/04/1995	
964008510	DK	22/04/1995	
964008510	GB	22/04/1995	
9504918	TR	22/04/1995	
108103620	US	21/02/1995	Minimizing program code storage for performing regular and repetitive operations in a programmable processor // Dunn, Puleo et al.
961003399	DE	19/02/1995	
981021357	FR	29/02/1995	
961003399	GB	19/02/1995	
6744654	US	02/07/1995	Apparatus and a method for forming and applying a thin film of a conductive material // Nohara, Kuroda et al.
961409347	GB	10/07/1995	
973408042	FR	04/07/1995	
109462716	US	08/07/1995	Method of minimising die corner effects by densifying the insulative layer // Schayenne, Gallard et al.
20005025339	JP	08/07/1995	
989364807	GB	08/07/1995	
989364807	IT	08/07/1995	
9708612	FR	08/07/1995	

5

BEST AVAILABLE COPY

<u>Filing Number</u>	<u>Country Code</u>	<u>Filing Date</u>	<u>Page 4 of 7</u> <u>Title / Inventors</u> <u>(US Patent)</u>
109/1023849	US	23/06/1998	process for obtaining a transistor having a silicon germanium channel layer with a gate length of 0.15 micrometers or less, the gate being made of polysilicon
984014324	EP	12/06/1998	Process for fabricating a metal oxide semiconductor field effect transistor
109107938	EP	25/06/1997	Process for fabricating a metal oxide semiconductor field effect transistor
109178579	EP	25/06/1998	Process for fabricating a metal oxide semiconductor field effect transistor
109163211	US	15/06/1998	Electrostatically balanced supply distribution frame for optical fiber
98401410101	EP	11/06/1998	Electrostatically balanced supply distribution frame for optical fiber
97-07892	EP	20/06/1997	Electrostatically balanced supply distribution frame for optical fiber
109134362	US	26/08/1992	Instrument for measuring the energy dissipated per unit length in the fibers of a cable
9910201202	US	23/08/1999	Instrument for measuring the energy dissipated per unit length in the fibers of a cable
9810768	INR	27/08/1998	Instrument for measuring the energy dissipated per unit length in the fibers of a cable
1092189167	US	12/04/1999	Electron beam method of signal processing devices for implementing given filter and use thereof
990008502	EP	08/04/1999	Electron beam method of signal processing devices for implementing given filter and use thereof
109165091	EP	12/04/1999	Electron beam method of signal processing devices for implementing given filter and use thereof
981045537	INR	10/01/1999	Electron beam method of signal processing devices for implementing given filter and use thereof
109936197	US	07/03/2000	Process for testing integrated circuits with access to the memory points of the circuit by terminals
99102322	EP	16/03/1999	Process for testing integrated circuits with access to the memory points of the circuit by terminals
109104000	IN	07/03/2000	Process for testing integrated circuits with access to the memory points of the circuit by terminals
00209421001	GB	07/03/2000	Process for testing integrated circuits with access to the memory points of the circuit by terminals
00209440101	IN	07/03/2000	Process for testing integrated circuits with access to the memory points of the circuit by terminals
10929722081	US	18/05/2000	Capacitive microphone/Microphone
99106122	EP	26/05/1999	Capacitive microphone/Microphone
009296200	GB	18/05/2000	Capacitive microphone/Microphone
0092962010	IN	18/05/2000	Capacitive microphone/Microphone
1096101330	US	23/02/2000	Dual mode radio frequency reception device and corresponding multimediate receiver
9901037791	EP	23/03/1999	Dual mode radio frequency reception device and corresponding multimediate receiver
002600169	EP	24/02/2000	Dual mode radio frequency reception device and corresponding multimediate receiver
10954011881	US	31/03/2000	Process for fabricating a planar electrostructure/
9901052	EP	31/03/1999	Process for fabricating a planar electrostructure/
1091660	JP	29/03/2000	Process for fabricating a planar electrostructure/
003007035	EP	14/03/2000	Process for fabricating a planar electrostructure/

5

BEST AVAILABLE COPY

<u>Filing Number</u>	<u>Country Code</u>	<u>Filing Date</u>	<u>Page 5 of 7</u> <u>Title / Inventors</u> <u>(US Patent)</u>
09/5591916	US	20/04/2000	Integrated circuit device comprising an inductor with high quality coefficient / Merck & Rohm Semiconductors
09/04986	EP	20/04/1999	
004010864	EP	19/04/2000	
1518650	EP	19/04/2000	
10/019170	US	23/06/2000	Method for compensating non-linearity of a sigma-delta and D/A digital converter / Monchalin
99/08323	EP	29/06/1999	
09/5189411	US	06/08/2000	Radio frequency transmitter with a high degree of linearity and low noise and possibly with self calibration and/or deletion of interference
004600193	EP	02/08/2000	
99/08768	EP	23/08/1999	
09/5591524	US	28/04/2000	Control system that stabilizes and input through a feedback loop and a modulator and voltage controlled oscillatory bus amplifiers
0020111003	EP	19/04/2000	
99/05627	EP	30/04/1999	
1201103	EP	28/04/2000	
02/305478	US	16/02/2000	Process for the anisotropic etching of an organic dielectric polymer film by a plasma and application thereof to microstructures / Tolbert, Laird
99/01098	EP	17/02/1999	
1030132	EP	16/02/2000	
004009628	EP	09/02/2000	
10/0181791	US	05/06/2000	Semiconductor device with compensated threshold voltage and method for making the same / Skotnicki, Gwozdzik
009533869	EP	05/06/2000	
99/07391	EP	11/06/1999	
09/9301027	US	26/05/2000	Radio-pass filter with carrier frequency reduction / Mbroto
009369321	GB	26/05/2000	
009369321	US	26/05/2000	
99/06710	EP	27/05/1999	
10/0186680	US	08/06/2000	Method for making a silicon substrate comprising a buried thin silicon oxide film / Jurezak, Skomak
99/07496	EP	14/06/1999	
009404575	EP	08/06/2000	

5

BEST AVAILABLE COPY

<u>Filing Number</u>	<u>Country Code</u>	<u>Filing Date</u>	<u>Page 6 of 7</u> <u>Title / Inventors</u> <u>(US Patent)</u>
09/6201896	US	21/07/2000	Method of correcting topographical effects on a microelectronic substrate // Schmitz, Ralf; Sonnystone, John
09/2195257	EP	19/07/2000	
2000-0012268	KR	22/07/2000	
00199092917	EP	22/07/1999	
10040205337	GB	19/07/2000	
00040205333	EP	19/07/2000	
10116810411	US	10/08/2001	Low noise spectroscopic cell detector // Romieu
00100712351	EP	09/08/2000	
10119455810	EP	08/06/2001	
2002-502401	JP	08/06/2001	
09/915210574	US	25/07/2000	Resin, a double resin layer, an extreme ultraviolet (EUV) photo-lithography and an extreme ultraviolet light (EUV) photolithography process // Schmitz
00140197235	EP	12/07/2000	
10040097530	BR	26/07/2000	
1043807567	US	18/09/2001	Device for optical measurement of an eddy current magnetic field by the constant amplitude and frequency method // Goto, Toshiyuki
00111109241	EP	19/09/2000	
101193992016	DE	18/09/2001	
101196022016	FR/JP	18/09/2001	
5238342/2002	JP	18/09/2001	
00193922016	GB	18/09/2001	
103321592	US	10/07/2001	Small island conductive film and its preparation method, optical circuit board and liquid crystal display source // Matsuyama
001091080	FR	11/07/2000	Conducting polymer, composition containing it, and its use // Duhme
0119540657	EP	10/07/2001	
2002-5098247	JP	10/07/2001	
127416127	CA	10/07/2001	
0110918291	EP	21/03/2001	Method and device for transmitting a video sequence comprising a face, especially in a mobile videophone system // Koury, Peter
0274168947	EP	14/03/2002	

F5

BEST AVAILABLE COPY

<u>Filing Number</u>	<u>Country Code</u>	<u>Filing Date</u>	<u>Page 7 of 7</u> <u>Title / Inventors</u> <u>(US Patent)</u>
10725973	US	24/05/2002	Large capacity automated distribution particularly for telephone lines and method for connection and disconnection of the binding lines/Méthode pour la connexion et la déconnexion des lignes de liaison
020617	FR	29/03/2001	
01107032	FR	29/03/2001	
02774873	EP	24/05/2002	
2003-500628	JP	24/05/2002	

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at Issy les Moulineaux
on December 3rd, 2004

ASSIGNOR

By: France Telecom

Name: François JAMET

Title: Directeur de la Propriété Industrielle et de la Valorisation

(Signature MUST be notarized)

Je soussigné Eric de La HAYE SAINT HILAIRE
Notaire à Paris, Certifie véritable
la signature de M. François JAMET
apposée ci-contre
Paris, le 3 décembre 2004.

[Handwritten signatures]